

## Oferta de Prácticas en empresa en Analog Devices, SLU. Curso 2020-21

Analog Devices, Inc. es una empresa líder mundial en el diseño y fabricación de circuitos microelectrónicos integrados y de sistemas basados en ellos.

Analog Devices, Inc. tiene un centro de desarrollo ubicado en el Parc Científic de la Universitat de Valencia en Paterna, Valencia. [www.analog.com/spain](http://www.analog.com/spain)

Analog Devices ofrece la posibilidad de realizar prácticas en empresa remuneradas en el curso 2020-21 con las siguientes características:

- Bolsa económica de 1,166Eur al mes para una dedicación de 40 horas semanales
- Incorporación flexible a partir de noviembre de 2020
- Duración entre 6 y 9 meses
- Vacaciones pagadas equivalentes a 22 días al año
- Idioma de trabajo inglés

Los candidatos deben ser estudiantes de últimos cursos de grado y master en titulaciones afines (Ingenierías en el área de Telecomunicación, Electrónica, Industrial, Biomédica, Informática, así como en los grados en Físicas y matemáticas). Los recién egresados también serán elegibles.

Si estás interesado en integrarte en una empresa líder, dentro de un entorno en el que se prima el trabajo en equipo y el crecimiento personal, envía tu CV, notas, y carta de presentación, todo ello en inglés, a la dirección [adispain\\_careers@analog.com](mailto:adispain_careers@analog.com)

El proceso de selección incluye una o varias entrevistas técnicas (este año se realizarán típicamente por videoconferencia)

Ofertas para el curso 2020-21:

Position	Description
Apps	Development of applications boards (hardware and software) for devices used in consumer products
Digital Des	Digital IC design. Familiar with Verilog or VHDL and FPGA.
Design Verif	Verification of digital systems embedded in Integrated Circuits
FW test	Medical Products team. The intern will participate in the development of a data gathering system to enable the team to collect data from medical environment. The intern will evaluate this platform and improve the automated test processes; and will participate in the definition of tests, their implementation, and debugging
System Apps	Medical Products team. The intern will participate in the development of a data gathering system to enable the team to collect data from medical environment. The intern will join the team developing this platform and will participate in the design, firmware development, testing and debugging of this system
Analog Design	Analog verification and modelling of ultrasound systems for medical applications
Design Verif	Develop hardware models using SystemVerilog and verification methodologies such as UVM (Universal Verification Methodology) for infotainment systems.
Analog Design	Support in the development of the motor drive signal chain from the ADC to the signal processing to the high-level algorithm

Nota: Las prácticas serán presenciales a menos que la situación sanitaria recomiende el teletrabajo de manera total o parcial.

## Internship offers in Analog Devices, SLU. Academic year 2020-21

Analog Devices, Inc. is a world leader in the design and manufacturing of microelectronics integrated circuits and subsystems based on them.

Analog Devices, Inc. has a Development centre in the Parc Científic de la Universitat de Valencia in Paterna, Valencia, Spain. [www.analog.com/spain](http://www.analog.com/spain)

Analog Devices offers the opportunity to complete paid industrial placements along the 2020-21 academic year with the following characteristics:

- Monthly economic compensation of 1,166Eur for a 40h per week
- Flexible start from November 2020
- Duration between 6 to 9 months
- Paid holidays equivalent to 22 days per year
- English as the working language

Candidates must be students in their last years of their degrees or masters in affine degrees (e.g. in Spain engineering degrees or masters in Telecommunications, Electronics, Industrial, Biomedical, Computer Science, as well as degrees in Physics and Mathematics). New grads may also be considered.

If you are interest in joining a world leader company and be part of an environment that encourages teamwork and personal development, please send your CV, marks, and a motivation letter (in English) to [adispain\\_careers@analog.com](mailto:adispain_careers@analog.com).

The selection process involves one or several technical interviews (this year they will typically be done through videoconference).

Offers for the academic year 2020-21:

Position	Description
Apps	Development of applications boards (hardware and software) for devices used in consumer products
Digital Des	Digital IC design. Familiar with Verilog or VHDL and FPGA.
Design Verif	Verification of digital systems embedded in Integrated Circuits
FW test	Medical Products team. The intern will participate in the development of a data gathering system to enable the team to collect data from medical environment. The intern will evaluate this platform and improve the automated test processes; and will participate in the definition of tests, their implementation, and debugging
System Apps	Medical Products team. The intern will participate in the development of a data gathering system to enable the team to collect data from medical environment. The intern will join the team developing this platform and will participate in the design, firmware development, testing and debugging of this system
Analog Design	Analog verification and modelling of ultrasound systems for medical applications
Design Verif	Develop hardware models using SystemVerilog and verification methodologies such as UVM (Universal Verification Methodology) for infotainment systems.
Analog Design	Support in the development of the motor drive signal chain from the ADC to the signal processing to the high-level algorithm

Note: The internships are presencial unless the pandemic forces total or partial telework.